	Application No.	Applicant(s)
Notice of Allowability	09/910,630	HUFFMAN, WILLIAM A.
	Examiner	Art Unit
	Pierre-Michel Bataille	2186
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	ears on the cover sheet with (OR REMAINS) CLOSED in the or other appropriate communication is sub-	the correspondence address nis application. If not included ication will be mailed in due course. THIS
1. This communication is responsive to <u>01/04/2005</u> .		
2. The allowed claim(s) is/are <u>1-20</u> .		
3. \boxtimes The drawings filed on <u>05 November 2001</u> are accepted by	the Examiner.	
 4. Acknowledgment is made of a claim for foreign priority una) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	been received. been received in Application	No
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give		
6. CORRECTED DRAWINGS (as "replacement sheets") mus	t be submitted.	
(a) \square including changes required by the Notice of Draftspers	on's Patent Drawing Review (PTO-948) attached
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date	•	
(b) including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or in	the Office action of
Identifying indicia such as the application number (see 37 CFR 1, each sheet. Replacement sheet(s) should be labeled as such in the		
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT	sit of BIOLOGICAL MATER	RIAL must be submitted. Note the
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	6. ☐ Interview Sum Paper No./Ma 8), 7. ☐ Examiner's Ar	rmal Patent Application (PTO-152) Imary (PTO-413), ail Date mendment/Comment atement of Reasons for Allowance

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DETAILED ACTION

Allowable Subject Matter

- 1. Claims 1-20 are allowed.
- 2. The following is an examiner's statement of reasons for allowance: the present invention features: a plurality of processor nodes; a memory, wherein the memory includes a plurality of lines and a cache coherence directory structure, wherein the plurality of lines includes a first line and wherein the cache coherence directory structure includes a plurality of directory structure entries; and an interconnect network connecting the plurality of processor nodes to the memory.

Closed related art:

- Kaplow et al, "Run-Time Reference Clustering for Cache performance

 Optimization", IEEE Proceeding on Parallel Algorithms, March 1997.
- Sterling et al., "Gilgamesh: A Multithreaded Processor-In-Memory Architecture for Petaflops Computing", http://sc-2002.org/paperpdfs/pap.pap105.pdf.
- US 5,596,704 (Geddes) suggests process models including processing nodes and process streams where a processor utilizes the process model to determine coordinates for each node and to determine the routing coordinates for each process stream for display and the coordinates defining a matrix with matrix rows and matrix columns, with each of the plurality of nodes associated with an associated matrix row and each of the plurality of nodes associated with an

associated matrix column.

US 5,367,692 (Edelman) suggests a processing element array comprising a plurality of processing element nodes the form of an "n"-dimensional hypercube, the dimensionality of the hypercube and the number of processing element nodes being related, with the relation being that the number of dimensions corresponds to the logarithm, to the base two, of the number of processing element nodes in the hypercube.

However, none of the prior art of record suggests or, in combination, renders obvious, as recited in the claims:

n bit vectors defining a matrix having a number of locations equal to the product of the number of bits in each of the n bit vectors, wherein the number of locations being at least equal to the number of processor nodes and wherein each of the processor nodes is mapped to a corresponding one of the locations wherein the locations corresponding to the processor nodes are dispersed in the matrix in an at least partially noncontiguous manner.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Pierre-Michel Bataille whose telephone number is (571) 272-4178. The examiner can normally be reached on Mon-Fri (9:30A to 6:00P).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew M. Kim can be reached on (571) 272-4182. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Pierre-Michel Bataille Primary Examiner Art Unit 2186

February 9, 2005

PIERRE BATAILLE PRIMARY EXAMINER